

**Amendments to the Claims:**

Please amend claims 1 and 16 as follows.

This listing of claims replaces all prior versions, and listings, of claims in the application.

**Listing of claims:**

1. (Currently Amended) A method of manufacturing a capacitor of a semiconductor device, the method comprising:

forming a first electrode on a semiconductor substrate;

depositing a first dielectric layer on the first electrode;

curing the first dielectric layer in an atmosphere containing oxygen;

depositing a second dielectric layer on the cured first dielectric layer using only a source gas without a reactant gas, wherein depositing the second dielectric includes introducing the semiconductor substrate into a deposition chamber, supplying a source gas to the deposition chamber and heating the semiconductor substrate such that a stable dielectric layer is deposited;  
and

after depositing the second dielectric layer, forming a second electrode on the second dielectric layer without curing the second dielectric layer.

2. (Original) The method as claimed in claim 1, wherein the first dielectric layer is deposited using only a source gas without a reactant gas.

3. (Original) The method as claimed in claim 1, wherein the first dielectric layer and the second dielectric layer are deposited using chemical vapor deposition.

4. (Original) The method as claimed in claim 1, wherein the first dielectric layer and

the second dielectric layer are deposited using atomic layer deposition.

5. (Original) The method as claimed in claim 1, wherein the source gas includes oxygen atoms.

6. (Original) The method as claimed in claim 1, wherein the first dielectric layer and the second dielectric layer are deposited at a temperature of 100 to 600 °C.

7. (Original) The method as claimed in claim 1, wherein the first dielectric layer is deposited to a thickness of 5 to 200 Å, and the second dielectric layer is deposited to a thickness of 5 to 3000 Å.

8. (Original) The method as claimed in claim 1, wherein the source gas is one of Ta(OC<sub>2</sub>H<sub>5</sub>)<sub>5</sub>, tetra ethoxide tantalum-dimethyl amine ethoxide, Ta(OsBu)<sub>5</sub>, Ta(OC<sub>2</sub>H<sub>5</sub>)<sub>4</sub>(acacC<sub>2</sub>H<sub>5</sub>), TaCl<sub>2</sub>(OC<sub>2</sub>H<sub>5</sub>)<sub>2</sub>C<sub>5</sub>H<sub>7</sub>O<sub>2</sub>, and Ta(OCH<sub>3</sub>)<sub>5</sub>.

9. (Original) The method as claimed in claim 1, wherein the first dielectric layer is formed of Ta<sub>2</sub>O<sub>5</sub> using chemical vapor deposition.

10. (Original) The method as claimed in claim 1, wherein the second dielectric layer is formed of Ta<sub>2</sub>O<sub>5</sub> using chemical vapor deposition.

11. (Original) The method as claimed in claim 1, wherein steps from depositing the first dielectric layer to depositing the second dielectric layer are performed in-situ in a single apparatus for forming dielectric layers.

12. (Original) The method as claimed in claim 1, wherein the atmosphere containing oxygen is an oxidative atmosphere containing O<sub>2</sub> or O<sub>3</sub>.

13. (Original) The method as claimed in claim 1, wherein the atmosphere containing oxygen is electron cyclotron resonance or an RF plasma of one of O<sub>2</sub> and N<sub>2</sub>O.

14. (Original) The method as claimed in claim 1, wherein the first electrode and the second electrode are formed of one of TiN, TaN, W, WN, Al, Cu, Ru, RuO<sub>2</sub>, Pt, Ir, IrO<sub>2</sub>, a doped polysilicon, and a combination thereof.

15. (Original) The method as claimed in claim 1, wherein the first dielectric layer and the second dielectric layer are formed of one of Ta<sub>2</sub>O<sub>5</sub>, HfO<sub>2</sub>, ZrO<sub>2</sub>, Al<sub>2</sub>O<sub>3</sub>, TiO<sub>2</sub>, and a combination thereof.

16. (Currently Amended) A method of manufacturing a capacitor of a semiconductor device, the method comprising:

forming a first electrode on a semiconductor substrate;  
depositing a first Ta<sub>2</sub>O<sub>5</sub> layer on the first electrode;  
curing the first Ta<sub>2</sub>O<sub>5</sub> layer in an O<sub>3</sub> atmosphere;  
depositing a second Ta<sub>2</sub>O<sub>5</sub> layer on the cured first Ta<sub>2</sub>O<sub>5</sub> layer using only Ta(OC<sub>2</sub>H<sub>5</sub>)<sub>5</sub> without a reactant gas, wherein depositing the second Ta<sub>2</sub>O<sub>5</sub> layer includes introducing the semiconductor substrate into a deposition chamber, supplying a source gas to the deposition chamber and heating the semiconductor substrate such that a stable Ta<sub>2</sub>O<sub>5</sub> layer is deposited; and  
after depositing the second Ta<sub>2</sub>O<sub>5</sub> layer, forming a second electrode on the second Ta<sub>2</sub>O<sub>5</sub> layer without curing the second Ta<sub>2</sub>O<sub>5</sub> layer.

17. (Original) The method as claimed in claim 16, wherein the first Ta<sub>2</sub>O<sub>5</sub> layer is deposited using only Ta(OC<sub>2</sub>H<sub>5</sub>)<sub>5</sub> without a reactant gas.

18. (Original) The method as claimed in claim 16, wherein the first Ta<sub>2</sub>O<sub>5</sub> layer and the second Ta<sub>2</sub>O<sub>5</sub> layer are deposited using chemical vapor deposition.

19. (Cancelled)

20. (Cancelled)